TECHNOLOGY MANAGEMENT SEMINAR (FEBRUARY 13- FRIDAY)

By Reynold V. D’sa – VP of Platform Engineering and GM of Devices Development, Intel Corp.

Reynold V. D’sa leads engineering teams responsible for designing Intel's phone, tablet and essential device products.

Since joining Intel in 1991 as a silicon design engineer, Reynold has held several technical and senior management positions in microprocessor design and development. He has led the development and delivery of multiple generations of Intel Core processors, including the processor families codenamed "Westmere" and "Haswell."

D'Sa holds 15 patents in computer architecture and design, and he received Intel Achievement Awards in 2002 and 2004 for his work with Intel processors. He earned his master's and bachelor's degrees in electrical engineering from Cornell University in 1991 and Oklahoma State University in 1990, respectively.

Leading Transformation

Reynold will talk about the transformation he led at Intel: a diverse organization spreading across multiple geographies to embark on a journey to transform into a nimble organization to deliver complex System on Chips for PC Client, Tablet and Essential device platforms, on a multiple product launches a year cadence.

He will share the leadership principles and systems perspective that was adopted to lead in existing markets, disrupt and win new markets.

February 13, Friday 3:15pm – 4:15pm Venue: Engineering Building #102

All our seminars are open to PSU Faculty, Staff & Students, and the Community.
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